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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TC 2800 MAIL ROOM

Applicant(s): M. FUNABASHI

Serial No.: 09/902,673

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Group: 2825

Examiner: L. Malsawma

#7/B
SPY/6

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

May 7, 2002

Sir:

In response to the Office Action mailed December 7, 2001, please amend
the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 1-19 without prejudice or disclaimer, and add the
following new claims to the application:

Sub 017
31
--20. A method of manufacturing a semiconductor integrated circuit
device, comprising the steps of: